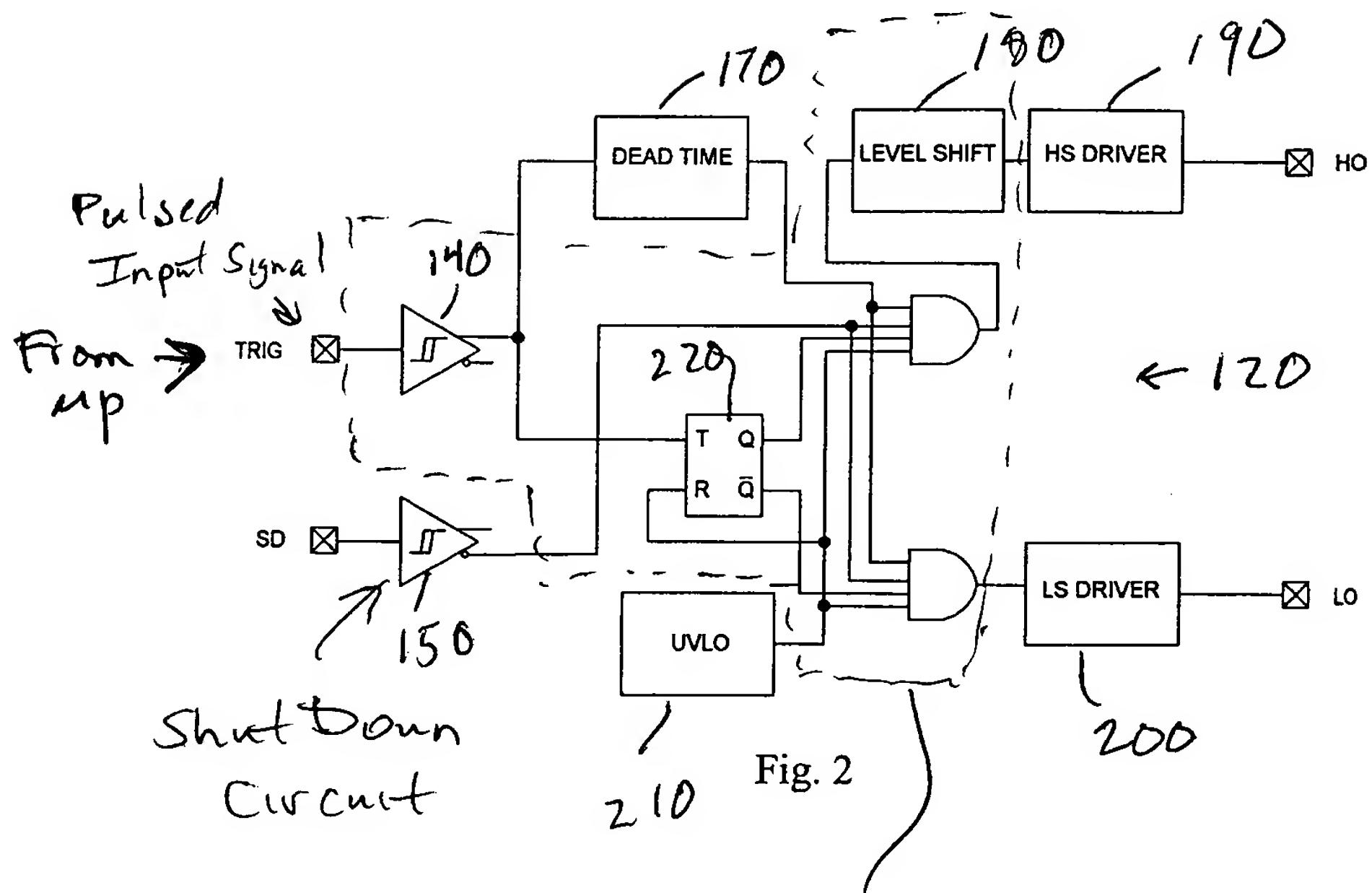


Fig. 1



Input Trigger  
Circuit